

#4
LTyson
6/13/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

JC715 U.S. PTO
09/716493
11/20/00

In re the Application of:

TI-30026

Gilbert Laurenti, et al.

Serial No:

Filed: November 20, 2000

For: Processor With Pointer Tracking to Eliminate Redundant Memory
Fetches

INFORMATION DISCLOSURE STATEMENT

Ass't Commissioner for Patents
Washington, DC 20231

EXPRESS MAILING® Mailing Label No. EL360244737. Date of Deposit: November 20, 2000. I hereby certify that this paper is being deposited with the U.S. Postal Service Express Mail Post Office to Addressee Service under 37 CFR 1.10 on the date shown above and is addressed to: Ass't Commissioner for Patents, Washington, D.C. 20231.


Robin E. Barnum

Dear Sir:

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. References have also been included from the Foreign Filing Search. Copies of the noted references are enclosed herewith.

Since this IDS is being submitted for any Office Action, no fee is due.

Respectfully submitted,



Gerald E. Laws
Attorney for Applicant
Reg. No. 39,268

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5287